

**IN THE ABSTRACT:**

The Abstract as amended below with a replacement Abstract shows added text with underlining and deleted text with ~~strikethrough~~.

~~Disclosed is a~~A clock regeneration circuit ~~comprising~~having a PLL circuit which includes a voltage control oscillator; a clock extraction circuit which includes a band passing filter and a harmonic component of a dividing signal of the oscillation frequency signal; a frequency detector; a filter; a bit rate detection circuit; and a frequency selection circuit outputting an oscillation frequency of the voltage control oscillator of the PLL circuit or a frequency signal obtained by dividing the oscillation frequency in response to the bit rate detected by the bit rate detection circuit, as a regeneration clock circuit.